



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroyuki NANSEL et al.**

Serial No.: **10/643,967**

Group Art Unit: **2822**

Filed: **August 20, 2003**

Examiner: **Toniae M. Thomas**

Confirmation No.: **4992**

For: **SEMICONDUCTOR MEMORY DEVICE AND METHOD FOR
MANUFACTURING SEMICONDUCTOR DEVICE**

RESPONSE TO RESTRICTION AND ELECTION REQUIREMENTS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

April 8, 2004

Sir:

This paper is submitted in response to the Official Action dated March 8, 2004.

In the Action, restriction is required between Group (I), Claims 1-29; and Group (II),
Claims 30-38.

Applicant hereby elects the subject matter of Group (I), Claims 1-29 and elects the Species i, i.e., claims 1-11. This election is made without traverse, it being understood that the applicant's rights to the filing of a divisional application directed to the non-elected subject matter under 35 USC 120 and 35 USC 121 are retained.

In the event that this paper is not timely filed, Applicants respectfully petition for an appropriate extension of time. Please charge any fees for such an extension of time and any other fees which may be due with respect to this paper, to Deposit Account No. 50-2866.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP



Stephen G. Adrian
Attorney for Applicants
Reg. No. 32,878

Attorney Docket No. **030993**

1250 Connecticut Avenue, N.W.
Suite 700
Washington, D.C. 20036
Telephone: (202) 822-1100

SGA/arf